

## **REMARKS**

Applicants respectfully traverse and request reconsideration.

Applicants wish to thank the Examiner for notice that claims 2-13, 22, 23 and 26 are allowed. As noted below, Applicants also respectfully submit that the remaining claims are also in condition for allowance for the reasons set forth.

Claims 14-16 and 24-25 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Cheney in view of So. Applicants have amended the independent claims to include inherent subject matter indicating, for example, that the secondary set of control signals are a set of memory control signals that control the storing of the compressed transport stream data signals into the memory buffer. As to claim 14, for example, the office action cites Cheney column 5, lines 52-62 as allegedly teaching the storing of compressed transport stream data controlled by a secondary set of control signals that were derived from the control signals in the transport stream. However, the cited portion instead merely states that the video FIFO 48 stores MPEG encoded data which is then stored in the DRAM 53. There is no discussion of analyzing the control signals within the transport stream of the MPEG stream and then determining which data to store in the DRAM or controlling the storage of the information in the DRAM based on another set of memory control signals derived from the control signals in the transport stream. There is no discussion of any secondary set of memory control signals being employed that are generated from the compressed transport streams control signals. Accordingly, Applicants respectfully submit that Cheney does not teach what is alleged and the claims are in condition for allowance.

Claims 18-20 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Schindler in view of So. The Schindler reference also fails to set forth the secondary set of memory control signals that are derived from the compressed transport stream control signals

and storing uncompressed data in a frame buffer memory in a different mode of operation as claimed. It is alleged that the So reference teaches this subject matter. However, the cited portion of So, namely column 17, lines 1-15 and column 130, line 15 to column 131, line 30 instead merely teach that MPEG streams can be decoded. It also sets forth the structure of a device. However, there is no mention of any memory control signals being derived from control signals in an a compressed transport stream, for example a DVB stream, and using those secondary memory control signals to store data or portions of the compressed stream in memory. Accordingly, Applicants respectfully submit that these claims are also in condition for allowance.

Claim 21 stands rejected under 35 U.S.C. § 103(a) as being unpatentable over Schindler in view of Malladi et al. and in further view of Datari. Claim 21 requires, among other things, storing pixel information in a frame buffer of a video graphics adapter wherein one line of frame buffer memory is representative of one line of video image to be displayed and in the second mode of operation stores compressed transport stream data wherein one line of the frame buffer memory is representative of one transport stream packet.

The motivation used to reject the claims must be relevant to Applicants' claimed invention. The motivation is not relevant to Applicants' device. As such, one would not use such motivation to combine references and as such, the references are not combinable as alleged.

The defendant claims add additional novel and non-obvious subject matter.

Applicants respectfully submit that the claims are in condition for allowance and that a timely Notice of Allowance be issued in this case. The Examiner is invited to contact the below-listed attorney if the Examiner believes that a telephone conference will advance the prosecution of this application.

Respectfully submitted,

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